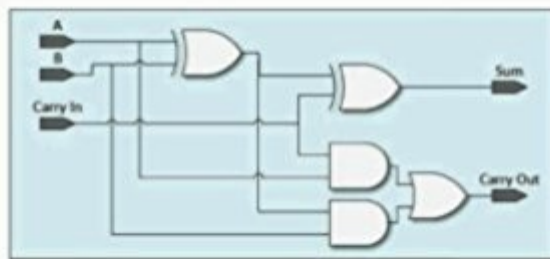


The book was found

Digital Logic RTL & Verilog Interview Questions

DIGITAL LOGIC RTL & VERILOG Interview Questions



A Practical Study Guide
for Design Engineers

verilogcode.com



Synopsis

Are you ready for your job interview? This book is a perfect study guide for digital design engineers or college students who want to practice real digital logic and RTL questions. The questions were put together first hand by a professional engineer based upon his own job search with top tier semiconductor companies. A wide range of information and topics are covered, including: RTL Verilog coding syntax, RTL Logic Design (including low power RTL design principles), clocking and reset circuits, clock domain crossing questions, digital design fundamentals, and logical thinking questions. The book contains over 50 digital interview questions, 41 figures and drawings, and 28 practical Verilog code examples, and is a perfect tool to help you succeed on your interview. By the end of this book, you will have the insight and knowledge of the types of digital design interview questions being asked in the field of semiconductor digital design today.

Book Information

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Customer Reviews

Very informative

Good and useful examples in the book. Could be updated with System Verilog examples.

Very interesting

Tiny book but covers a broad and important topics in the field of Verilog. It also provides author's notes on specific topics and give you an idea that what can an alternative design on certain question or what in this question you need to pay attention to. Five stars, recommended for every

Verilog learner who wants to find an entry-level position using Verilog.

This is a great book with lots of information and material about Verilog and digital design. The book is packed with questions and drawings/figures/timing diagrams and example code. I do feel more prepared now from reading this book, and I would highly recommend it to others.

GOOD JOB

not recommended. very very basic

not useful. waste money

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